

**CLAIMS**

1. An apparatus having a first mode of operation and a second mode of operation comprising:
  - 5 a data bus;
  - a first power supply operating in said first mode;
  - a second power supply operating in said first mode;
  - a third power supply operating in said first mode and said second mode; and
  - 10 a transistor with a base, collector and emitter wherein said first power supply is electrically coupled to the base, the second power supply being electrically coupled to the collector, the signal line being electrically coupled to the emitter and the third power supply being electrically coupled to the signal line wherein said transistor exhibits a conductive state during said first mode and exhibits a non conductive state during
  - 15 said second mode.
2. The apparatus of claim 1 wherein the signal line is connected to the collector via a resistor.
3. The apparatus of claim 2 wherein the resistor is located within a device.
- 20 4. The apparatus of claim 3 wherein the device is an integrated circuit.
5. An apparatus comprising a switch responsive to a first power supply voltage level wherein said switch electrically isolates a data bus from a second power supply in a first mode of operation and electrically connects said data bus to said second power supply in a second mode of operation.
- 25 6. The apparatus of claim 5 wherein the switch is a transistor.
7. The apparatus of claim 5 wherein the data bus is connected to the switch via a resistor.
8. The apparatus of claim 7 wherein the resistor is located within a device.
9. The apparatus of claim 8 wherein the device is an integrated circuit.

10. The apparatus of claim 5 wherein a plurality of switches electrically isolate a plurality of data bus lines from the second power supply in a first mode of operation and electrically connect a plurality of data bus lines to the second power supply in a second mode of operation.
- 5 11. An apparatus comprising a switch responsive to a control signal wherein said switch electrically isolates a data bus from a power supply in a first mode of operation and electrically connects said data bus to said power supply in a second mode of operation.
12. The apparatus of claim 11 wherein the switch is a transistor.
- 10 13. The apparatus of claim 11 wherein the data bus is connected to the switch via a resistor.
14. The apparatus of claim 13 wherein the resistor is located within a device.
15. The apparatus of claim 14 wherein the device is an integrated circuit.
- 15 16. The apparatus of claim 11 wherein a plurality of switches electrically isolate a plurality of data bus lines from the power supply in a first mode of operation and electrically connect a plurality of data bus lines to the power supply in a second mode of operation.
- 20 17. A television signal processing apparatus having a first device operative in a first mode of operation and a second device operative in said first mode of operation and a second mode of operation wherein said first device and said second device are both connected to at least one data bus line wherein said data bus line is connected to a first power supply via a first resistor integrated within said first device and said data bus line is connected to a second power supply via a second resistor integrated within said second device wherein said first resistor is electrically isolated from said first power supply during said second mode of operation and electrically connected to said first power supply during said second mode of operation.
- 25 18. The apparatus of claim 17 wherein the first resistor is electrically isolated from said first power supply during said second mode of operation and electrically

connected to said first power supply during said second mode of operation by a switch.

19. The apparatus of claim 18 wherein the switch is a transistor.

20. The apparatus of claim 18 wherein the switch is responsive to the first power  
5 supply.

21. The apparatus of claim 18 wherein the switch is responsive to a third power supply.

22. The apparatus of claim 18 wherein the switch is responsive to a control signal.

23. The television signal processing apparatus of claim 17 wherein said first  
10 device is an integrated circuit.

24. The television signal processing apparatus of claim 18 wherein said second device is an integrated circuit.